Abstract: This presentation discusses the drivers for consumer digital storage, the different mobile and static usage models for digital storage in consumer devices and the resulting consumer storage hierarchy. Important characteristics of consumer storage devices are shown and guidelines are given for how digital storage should be designed in consumer devices. Demand for higher resolution content and for capturing ever greater details of the life of family members will drive increases in commercial as well as personal content storage demand. Sharing of content within a home or over the Internet creates much greater demand for storage since a shared file can be multiplied many times through network sharing. Implementation of a virtualized integrated storage utility into most homes with appropriate ease of use, suitable for consumers, will benefit customers by providing greater access to data as well as enhanced content protection using local as well as remote storage.

About the speaker:

Tom Coughlin, President, Coughlin Associates is a storage analyst and consultant. He has over 30 years in the data storage industry with multiple engineering and management positions at high profile companies.

Dr. Coughlin has many publications and six patents to his credit. Tom is also the author of Digital Storage in Consumer Electronics: The Essential Guide, which was published by Newnes Press in March 2008. Coughlin Associates provides market and technology analysis (including reports on several digital storage technologies and applications and a newsletter) as well as Data Storage Technical Consulting services.

Tom is active with SMPTE, IDEMA, SNIA, the IEEE Magnetics Society, IEEE CE Society (Adcom member, TPC member, Associate Editor and VP of Operations and Planning), and other professional organizations. Tom is the founder and organizer of the Annual Storage Visions Conference (www.storagevisions.com), a partner to the annual Consumer Electronics Show as well as the Creative Storage (www.creativestorage.org). Tom has also been the chairman of the annual Flash Memory Summit.

He received a B.S. in Physics and an M.S.E.E. from the University of Minnesota (Minneapolis) and a PhD in Electrical Engineering from Shinshu University in Nagano, Japan.